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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of : Brian Day et al.  
Serial No. : 10/695,929  
Examiner : Phan, Trong Q.  
Art Unit : 2827  
Filed : October 28, 2003  
For : CMOS ISOLATION CELL FOR EMBEDDED MEMORY  
IN POWER FAILURE ENVIRONMENTS

Mail Stop Amendment  
Commissioner For Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**AMENDMENT AND RESPONSE TO THE OFFICE ACTION**

**DATED MAY 13, 2005**

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**CERTIFICATE OF MAILING/TRANSMISSION (37 C.F.R. § 1.8)**

I hereby certify that, on the date shown below, this correspondence is being:

**MAILING**

☒ deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

*ReNea D. Berggren*  
ReNea D. Berggren

**FACSIMILE**

☐ transmitted by facsimile to the Patent and Trademark Office.

DATED: July 11, 2005

Dear Sir:

This is in response to the Office Action dated May 13, 2005, for which the three-month shortened statutory period is set to expire on August 13, 2005. While Applicant believes that no extension of time for response is necessary, should the Office determine that any additional fees are necessary for this response, the Commissioner is hereby authorized to charge any deficiencies to Deposit Account No. 12-2252.

Dear Sir:

In response to the Office Action of May 13, 2005, please amend the above-identified application as follows:

**Amendments to the Specification** begin on page 3 of this paper.

**Amendments to the Claims** are reflected in the listing of claims which begins on page 7 of this paper.

**Remarks/Arguments** begin on page 9 of this paper.